

Amendments to the Specification

On page 13, the paragraph beginning at line 6, please substitute the following paragraph:

A1  
--During a first cycle, the multiplier 310 may take a first set of multiplication operands 302 and combine them to produce a first result that is stored in the pre-accumulation register 320. During a subsequent cycle, the multiplier 310 may receive a new set of [to] operands that it combines to produce a new result. The pre-accumulation register 320 allows the previous result computed by the multiplier 310 to be held for use in a subsequent operation by the adder 340. As such, a function such as  $(A \times B) + (C \times D)$  could be performed. In such an example, A and B may be the first set of multiplication operands where the result of the multiplication of A and B is temporarily stored in the pre-accumulation register 310 [320]. During the subsequent cycle, C and D are multiplied by the multiplier 320, and the result is supplied to the adder 340 along with the previous result  $(A \times B)$ , which is stored in the pre-accumulation register 320. The adder 340 can then add these two operands,  $(A \times B)$  and  $(C \times D)$  to produce the desired result. The result may be a final result 342 that is passed on to other circuitry, or an intermediate result that is stored in the memory 350 or the accumulation buffer 360 for further processing by the circuit

300.--